

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : VELLA et al.  
Serial No. : 10/622,370  
Filed : July 18, 2003  
Title : SEMICONDUCTOR

Art Unit : 2176  
Examiner : Peter J. Smith

## INFORMATION DISCLOSURE STATEMENT

The enclosed Information Disclosure Statement was originally filed on July 17, 2006, but was never entered in PAIR and thus not considered by the examiner.

This filing is a resubmission and as such no fee [Fee Code 1806 37 CFR 1.17(p)] is required.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Please apply any other appropriate charges or credits to Deposit Account No. 50-3000.

Respectfully submitted,

Date: July 13, 2007

/Brian N. Young/

Brian N. Young  
Reg. No. 48,602

TRELLIS INTELLECTUAL PROPERTY LAW GROUP, PC  
1900 Embarcadero Rd., Suite 109  
Palo Alto, CA 94303  
Tel.: 650-842-0300  
Fax: 650-842-0304